

**DR8-EA  
buffered digital I/O  
engineering drawings**

# MASTER DRAWING LIST

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DWG. NO.	REV. LET.	NO. OF SHEETS	TITLE
A-PL-DR8-EA-Ø	C	1	12 BIT DIGITAL I/O (DR8-EA)
E-CS-M863-Ø-1	#	2	12 BIT BUFFERED DIGITAL I/O
A-SP-DR8-EA-1	REF	12	ENGINEERING SPECIFICATIONS
A-SP-DR8-EA-2	REF	6	ACCEPTANCE SPECIFICATIONS
A-AL-DR8-EA-3		1	ACCESSORY LIST (DR8-EA)

REVISIONS				DRN.	DATE	<div style="display: flex; align-items: center; justify-content: center;"> <div style="font-weight: bold; font-size: 1.2em; margin-right: 5px;">digital</div> <div style="text-align: right; margin-right: 10px;">EQUIPMENT CORPORATION</div> </div> <small>MAYNARD, MASSACHUSETTS</small>	TITLE								
REV.	DATE	CHG. NO.	APP'D.	CHK'D.	DATE		<p>12 BIT DIGITAL I/O (DR8-EA)</p> <table style="width: 100%; border: none;"> <tr> <td style="border: none; width: 30px;">SIZE</td> <td style="border: none; width: 30px;">CODE</td> <td style="border: none; width: 80px;">NUMBER</td> <td style="border: none; width: 30px;">REV.</td> </tr> <tr> <td style="border: none; text-align: center;">A</td> <td style="border: none; text-align: center;">ML</td> <td style="border: none; text-align: center;">DR8-EA</td> <td style="border: none; text-align: center;">D</td> </tr> </table>	SIZE	CODE	NUMBER	REV.	A	ML	DR8-EA	D
SIZE	CODE	NUMBER	REV.												
A	ML	DR8-EA	D												
A	5/71	DR8E-1	G.B.	<i>K.S.</i>	4-28-71										
B	11/71	DR8E-2	A.W.	<i>K.S.</i>	7-28-71										
C	11/71	DR8E-3	A.W.	<i>K.S.</i>	7-28-71										
D	1/72	LAB8E-13	G.B.	<i>K.S.</i>	4-28-72										
FIRST USED ON				L888-E											
SCALE															
SHEET 1 OF 1				DIST.											

**DIGITAL EQUIPMENT CORPORATION**  
 MAYNARD, MASSACHUSETTS  
**PARTS LIST**

**QUANTITY / VARIATION**

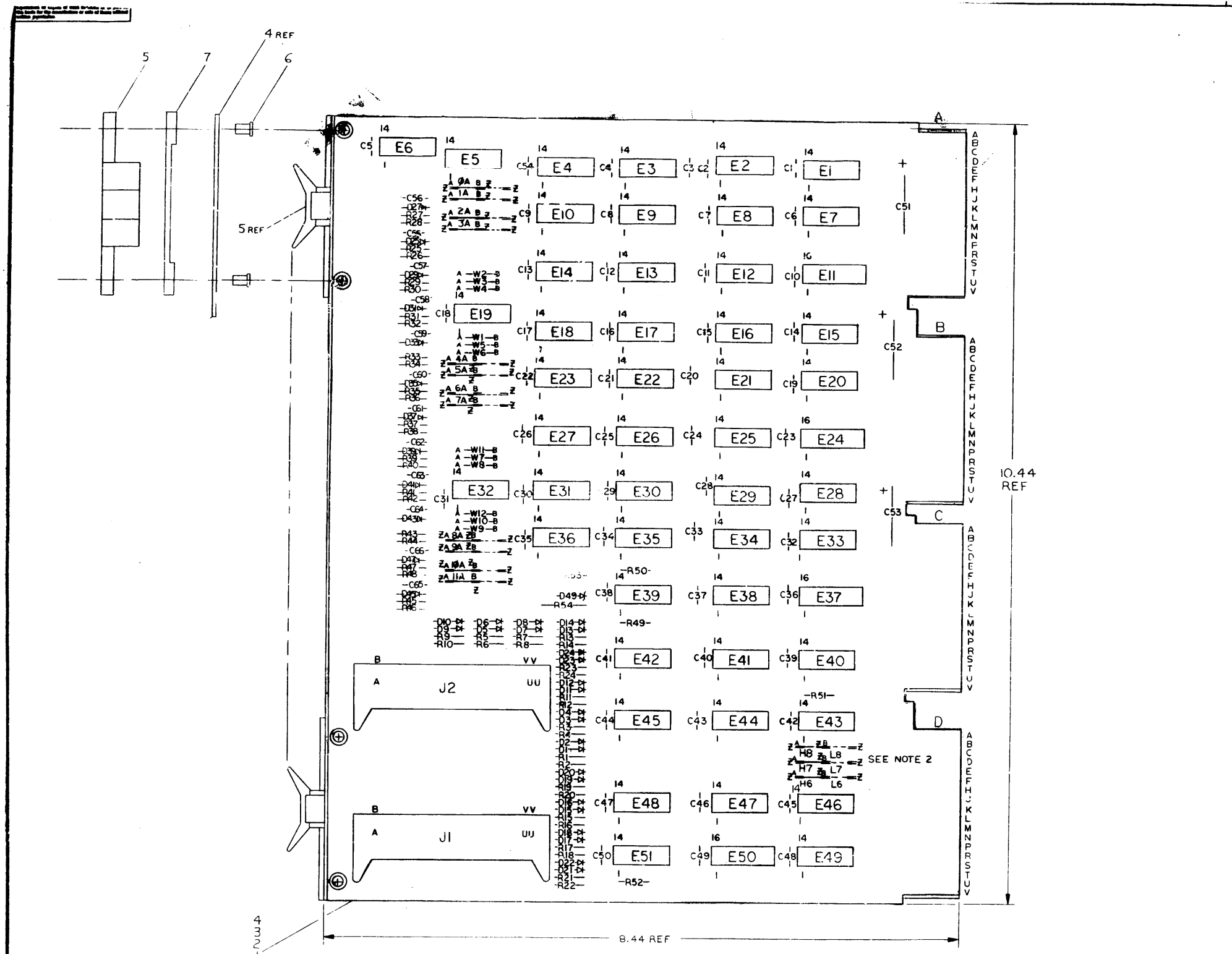
MADE BY	D.K. CRABBE	CHECKED	D.K. CRABBE	SECTION
DATE	4/7/71	DATE	4/28/71	1
ENG	D.K. CRABBE	PROD	P. FAZIO	ISSUED SECT.
DATE	4/28/71	DATE	5/3/71	1

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION	QUANTITY	UNIT	VARIATION
1	M863	12 BIT BUFFERED DIGITAL I/O	1		
2	C-IA-7008418-0-0	I/O CABLE ASSY. (BC08S DIAG. JUMPER)	1		
3	BC08J-10	I/O CABLE ASSY.	2		

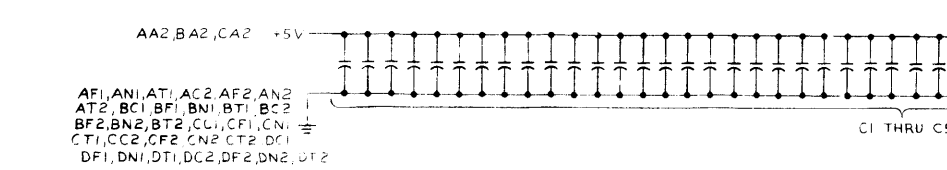
TITLE	ASSY NO.	SIZE	CODE	NUMBER	REV
12 BIT DIGITAL I/O (DR8-EA)		A	PL	DR8-EA-0	
	SHEET 1 OF 1	DIST.			

DEC FORM 200 10-69 (REV. 1-70)

NOTES:  
 1. UNLESS OTHERWISE NOTED:  
 CAPACITORS ARE .01μF, 100V, 20% DISC  
 DIODES ARE D664  
 RESISTORS ARE 1/4W, 5%  
 2. JUMPERS L6, L7 AND L8 ARE OPTIONAL



30R31	22	W3-A	W3-B
30R31	↑	W2-A	W2-B
30R31		W1-A	W1-B
30		H8-A	H8-B
		H7-A	H7-B
		H6-A	H6-B
		H5-A	H5-B
		H4-A	H4-B
		H3-A	H3-B
		H2-A	H2-B
		H1-A	H1-B
		H0-A	H0-B
		9A-A	9A-B
		8A-A	8A-B
		7A-A	7A-B
		6A-A	6A-B
		5A-A	5A-B
		4A-A	4A-B
		3A-A	3A-B
		2A-A	2A-B
		1A-A	1A-B
		0A-A	0A-B
		30R31	22
		W2-A	W2-B
		W1-A	W1-B
		W0-A	W0-B
		W9-A	W9-B
		W8-A	W8-B
		W7-A	W7-B
		W6-A	W6-B
		W5-A	W5-B
		W4-A	W4-B



QTY	REF DESIGNATION	DESCRIPTION	PART NO.
45		SPLIT LUGS	9006735
12	C55 THRU C66	CAP .01μF 100V 5% DM	1000027
AR		WIRE, BUSSING #22AWG	9107560-01
2	J1, J2	CONNECTOR 40PIN BERG	1209941
AR		TEFLON TUBING	9107265
36	D1 THRU D25, D27, D29, D31, D33, D35, D37, D39 THRU D41, D43, D45, D47	DIODE D664	1100114
1	D49	DIODE 1N746A 3.3V 5%	1104860
51	C1 THRU C50, C54	CAP .01μF 100V 20% DISC	1001610
3	C51, C52, C53	CAP .68μF 35V 10%	1005306
24	R2, R4, R6, R8, R10, R12, R14, R16, R18, R20, R22, R24, R26, R28, R30, R32, R34, R36, R38, R40, R42, R44, R46, R48	RES 47 1/4W 10%	1300204
1	R54	RES 82 1/2W 10%	1300225
4	R49, R50, R51, R52	RES 1K 1/4W 5%	1300365
13	R25, R27, R29, R31, R33, R35, R37, R39, R41, R43, R45, R47, R53	RES 3.3K 1/4W 5%	1300439
12	R1, R3, R5, R7, R9, R11, R13, R15, R17, R19, R21, R23	RES 470 1/4W, 5%	1300316
13	E1, E2, E3, E4, E5, E6, E7, E8, E9, E10, E11, E12, E13, E14, E15, E16, E17, E18, E19, E20, E21, E22, E23, E24, E25, E26, E27, E28, E29, E30, E31, E32, E33, E34, E35, E36, E37, E38, E39, E40, E41, E42, E43, E44, E45, E46, E47, E48, E49, E50, E51	IC DEC 7474	1905547
8	E28, E16, E21, E29, E34, E42, E47	IC DEC 7400	1905575
7	E3, E9, E17, E22, E30, E35, E41	IC DEC 7402	1909004
6	E12, E25, E38, E40, E43, E49	IC DEC 390	1909485
1	E50	IC DEC 8251	1909594
6	E13, E14, E26, E27, E39, E44	IC DEC 7404	1909686
1	E46	IC DEC 314	1909704
2	E45, E48	IC DEC 8881	1909705
2	E5, E6	IC DEC 7416	1909928
2	E19, E32	IC DEC 7405	1909930
3	E11, E24, E37	IC DEC 8235	1909935
45		SPLIT LUGS	9006735
4		SPACER (CABLE CLAMP)	1202704
4		EYELET .634-11 STIMPSON	9006750
4		HANDLE (FLIP CHIP) MAGENTA	9008337-06
1		ETCHED CIRCUIT BOARD	5009339
REF		MODULE ECO HISTORY	B-MH-M863-0-3
REF		ASSY/DRILLING HOLE LAYOUT	D-MH-M863-0-2
REF		X-Y COORDINATE HOLE LOC	K-CO-M863-0-1

FIRST USED ON OPTION/MOD

LAB #/E

ETCH BOARD REV E

SEMICONDUCTOR CONVERSION CHART

D664 IIN3606

DEC NO. EIA NO. DEC NO. EIA NO.

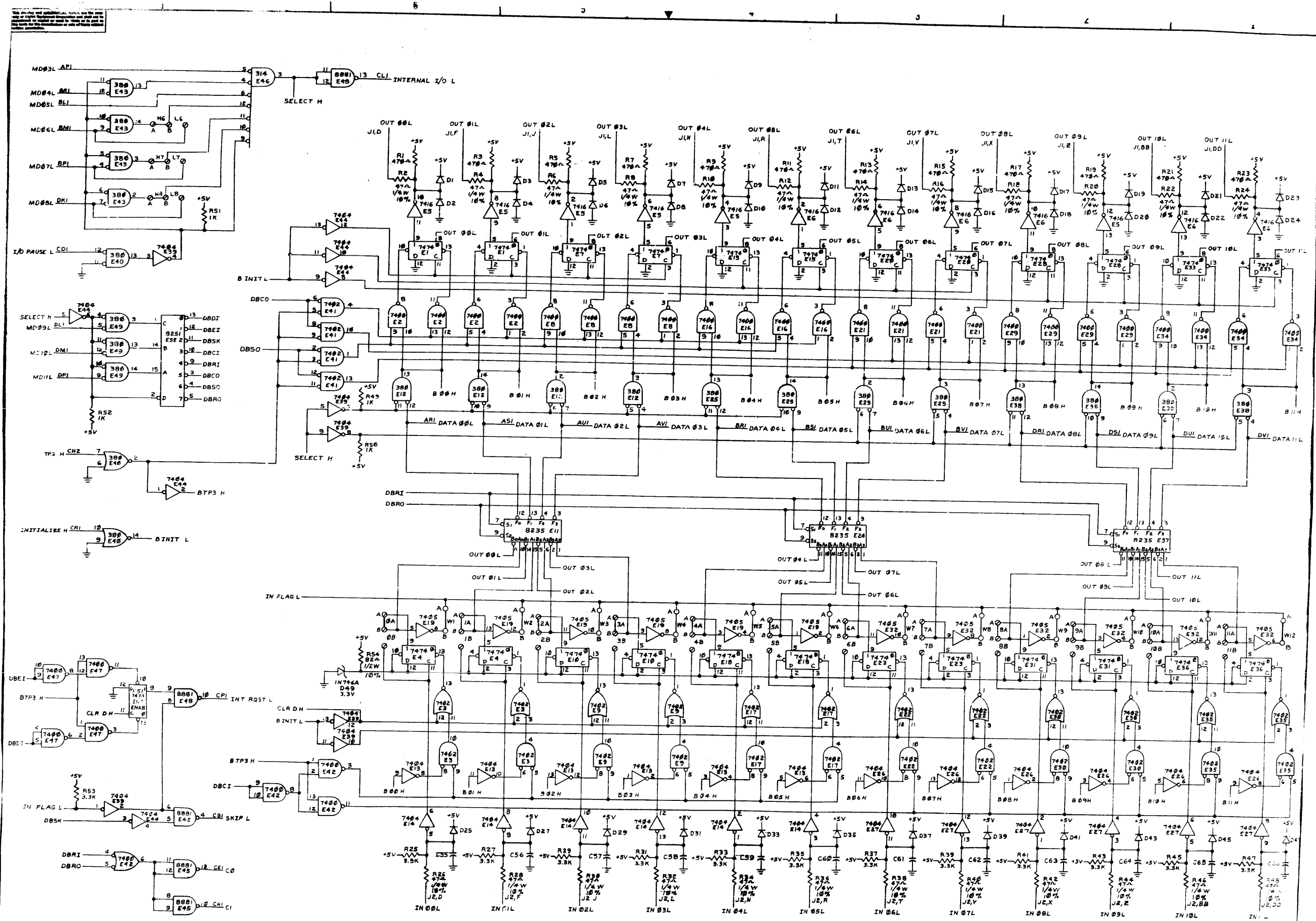
SCALE 2/1

DR8-EA

ECS M863-0-1

EQUIPMENT CORPORATION

12 CHANNEL BUFFERED I/O DR8-EA



REV.	QTY.	DESCRIPTION	PART NO.	REV.
1		PDP-8/E		
2				
3				
4				
5				
6				
7				
8				

UNLESS OTHERWISE SPECIFIED	DATE: 11-23-72	
TOLERANCES	BY: [Signature]	
RESISTORS	1/2% 1/4W	<b>12 CHANNEL BUFFERED I/O DR8-EA</b>
CAPACITORS	10% 1/4W	
IC'S	7400, 7402, 7404, 7408	<b>ICS M863-0-1</b>
OTHER		

